| | Application No. | Applicant(s) |
|---|--|--|
| | 10/604,328 | BLANCO ET AL. |
| Notice of Allowability | Examiner | Art Unit |
| | Nitin C. Patel | 2116 |
| The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RID of the Office or upon petition by the applicant. See 37 CFR 1.313 | (OR REMAINS) CLOSED in the or other appropriate communic IGHTS. This application is subj | is application. If not included cation will be mailed in due course. THIS |
| 1. This communication is responsive to <u>communication filed</u> of | on 7/11/03. | |
| 2. The allowed claim(s) is/are 1-28. | | |
| 3. Acknowledgment is made of a claim for foreign priority unallocation and allocation by Some* c) None of the: Certified copies of the priority documents have Certified copies of the priority documents have Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: | e been received. e been received in Application N | lo |
| Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. | | eply complying with the requirements |
| 4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give | | |
| 5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must (a) ☐ including changes required by the Notice of Draftspers 1) ☐ hereto or 2) ☐ to Paper No./Mail Date (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the paper No./Mail Date | on's Patent Drawing Review (for some some some some some some some some | the Office action of rawings in the front (not the back) of .121(d). |
| 6. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT | | |
| Attachment(s) | T □ Nadia a of lufa wa | a al Datant Annillo di an (DTO 450) |
| Notice of References Cited (PTO-892) Dotice of Draftperson's Patent Drawing Review (PTO-948) | 5. ☐ Notice of Inform 6. ☐ Interview Sumr | nal Patent Application (PTO-152) nary (PTO-413). |
| 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 | Paper No./Mai | Date <u>6/19/06</u> . |
| Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material | <u></u> | tement of Reasons for Allowance |
| U.S. Patent and Trademark Office | | |

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DETAILED ACTION

1. Claims 1 - 28 are presented for the examination.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Friedman Jack [reg. # 44688 on 19 June 2006.

The application has been amended as follows:

In the claims:

- 2. In the claim 16, delete after the word "device" ----; The method of claim 16, wherein the first memory device and the second memory device are buffers. ----in lines 10 12 on page 6 of claims.
- 3. In the claim 16, insert after the word "device" ----upon completion of said transmitting of the processed data to the output islands. ----in line 10 on page 6 of claims.

Reasons For Allowance

4. The following is an examiner's statement of reasons for allowance: Applicant's claimed invention distinguishes over the prior art for following reasons.

The independent claims 1, 9, and 15 are allowable over the art of record and none of the references either alone or in combination fails to teach or fairly suggest or

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render obvious a semiconductor device and method including a structure with an input island adapted to enable a means for providing a first specified voltage for powering a processing island after accepting the specified amount of data, a processing island is adapted to receive and process the specified amount of data from input island, and adapted to transmit the processed data to an output island upon powering by the second specified voltage and the first specified voltage is adapted to be disabled by removing power from processing island upon completion of transmission of the processed data as described in claimed invention.

The independent claim 23 are allowable over the art of record and none of the references either alone or in combination fails to teach or fairly suggest or render obvious a method including providing a plurality of memory islands, a plurality of processing islands, and a control island formed within a semiconductor device including collecting specified amount of data by each individual memory island, receiving and processing individually specified amount of data from each of corresponding memory islands, and controlling and supervising data transfer by control island by enabling and disabling plurality of voltages for powering each of the memory islands and processing islands as described in claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin C. Patel whose telephone number is 571-272-3675. The examiner can normally be reached on 6:30 am - 5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H. Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Nitin C. Patel June 19, 2006 LYNNE H. BROWNE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100